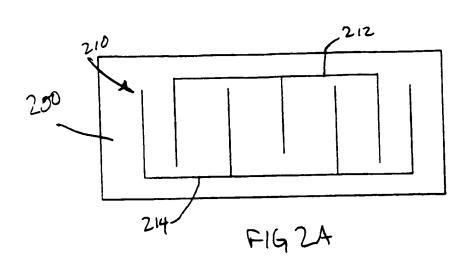
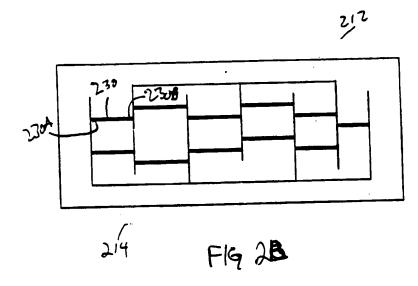
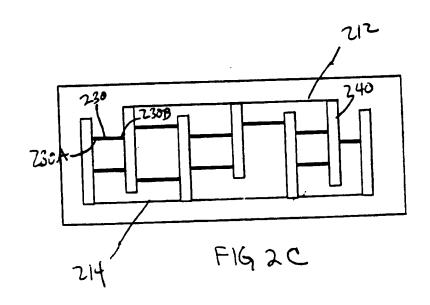
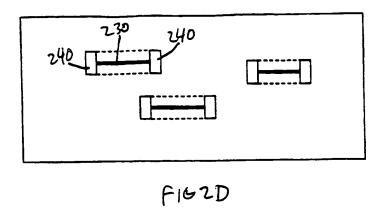


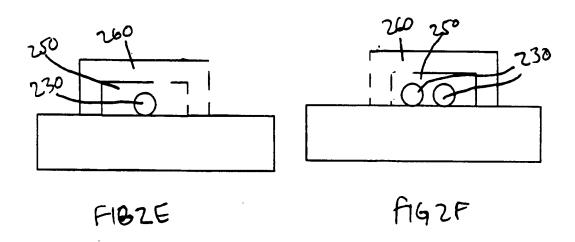
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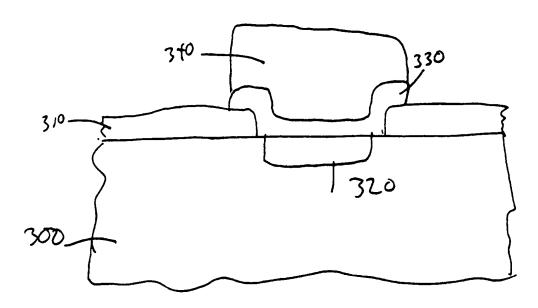




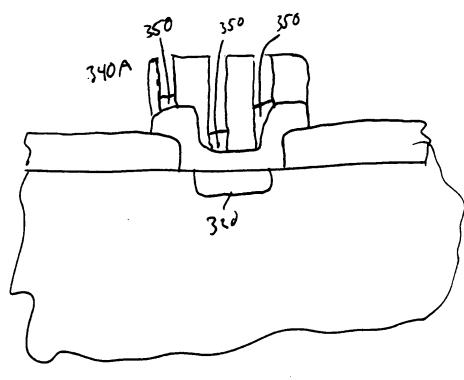




Applicant(s): Valery M. Dubin NANOTUBES FOR INTEGRATED CIRCUITS

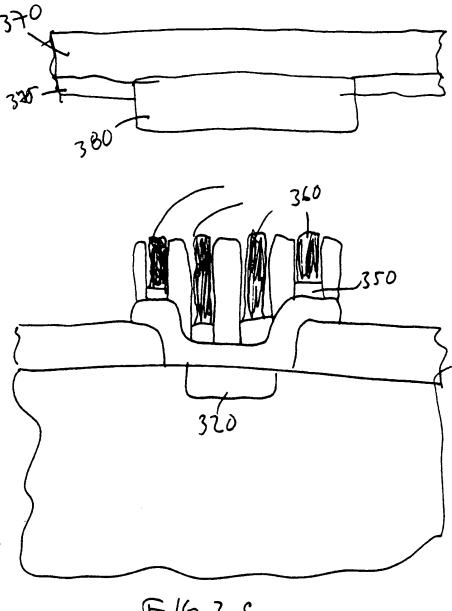


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